

SIZE	QTY	SYM	PLATED	TOL
0.01	470	+	YES	+/-0.003
0.187	4	X	NO	+/-0.003
0.006	24	◇	YES	+/-0.003
0.035	12	⊗	YES	+/-0.003
0.07	2	⊠	NO	+/-0.003
0.215	4	+ ^A	YES	+/-0.003
0.14173	4	+ ^D	YES	+/-0.003

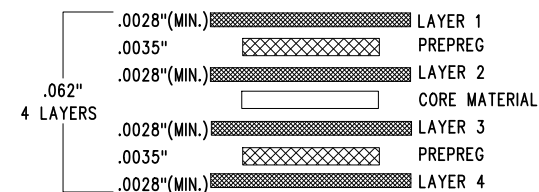
ADDITIONAL REQUIREMENT FOR PROTOTYPE FAB ONLY:

1. OUTGOING INSPECTION REPORT (BASED ON ACTUAL MEASUREMENTS AND CROSS SECTION).

ADDITIONAL REQUIREMENTS FOR PRODUCTION FAB ONLY:

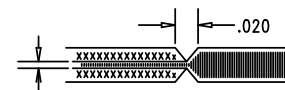
1. PROVIDE COMPLIANCE CERTIFICATES FOR RoHS, REACH AND CONFLICT-FREE MINERALS.
2. SOLDERABILITY BOARD WITH TEST RESULTS.
3. OUTGOING INSPECTION REPORT (BASED ON ACTUAL MEASUREMENTS AND CROSS SECTION).
4. VACUUM PACKED WITH DESICCANT.
5. FULL PANEL WITH NO REJECT.

LAYER STRUCTURE



NOTES: UNLESS OTHERWISE SPECIFIED

1. FAB PER IPC-A-600.
2. MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.
-FINISHED THICKNESS TO BE 0.062 +/- .005
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON ALL LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
3. SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00" ARE PRIMARY DATUMS.
4. DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001 THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003 IN RELATION TO CENTER
-FOR VIAS SIZE LESS THAN 13MILS, VIA HOLES NEED TO BE PLUGGED AND COVERED WITH SOLDERMASK.
5. FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
-FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
6. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
7. PCBs ARE TO BE RoHS COMPLIANT.
8. SCORING FOR PANELIZED PCB (PRODUCTION FAB ONLY):



UNLESS OTHERWISE SPECIFIED		ANALOG DEVICES www.analog.com	
DIMENSIONS ARE IN INCHES		TITLE: FABRICATION DRAWING	
TOLERANCES:		18V, 8A MONOLITHIC SYNCHRONOUS STEP-DOWN	
0.XX" = ±0.01"		SILENT SWITCHER 3 WITH LOW NOISE REFERENCE	
0.XXX" = ±0.005"			
INTERPRET DIM AND TOL PER ASME Y14.5M-1994			
THIRD ANGLE PROJECTION			
		SIZE	IC NO.
		N/A	LT8625SPJV-1
			DC3220A
			REV 1
		SHT 1 OF 1	